



Lemgo, 30 April 2020

Dear Students of the Department of Electrical Engineering and Computer Science,

As you may have noticed, we unfortunately had to revise the announcements concerning an alternative examination period for the week from 25 to 29 May 2020. The idea of transferring Examination Period 2, which could not take place in March, as a one-to-one copy to the end of May was unfortunately by far too optimistic. We would like to apologize to you for this misjudgment. Organizing all face-to-face examinations of Examination Period 2 is unfortunately not feasible at the end of May, because due to the hygiene rules that need to be applied, additional requirements for rooms and staff as well as a complete rescheduling would be necessary in combination with a considerable extension of time. In particular, this cannot be reconciled with the necessary time span for your preparation.

Our objective is to offer you the opportunity to take the necessary exams as soon as possible, under fair and safe circumstances and with the greatest possible planning security. This is extremely difficult in the current situation due to its dynamic changes. However, we would like to assure you that the Deanery and the Chairs of the Examination Boards, in cooperation with the university's Presidential Board, are involved in a continuous coordination process and that we are trying hard to find a reasonable solution for you. Unfortunately, there will be no optimal solution.

Within the framework of our effort to keep you up to date, it must also be clear that we are always dealing with planning statuses that we have drawn up to the best of our knowledge, but for which there can currently be no guarantee.

The following planning is currently in progress:

- 1. In accordance with the currently applicable rules, we will go into a carefully planned Examination Period 3 (presumably in July) with the majority of the canceled exams.
- 2. Forms of examination can currently be converted into non-bureaucratic formats that can be carried out online if necessary. Unfortunately, this is hardly applicable to the majority of exams at our Department due to the contents to be examined and the number of participants. Nevertheless, we take into consideration that such forms of examination might be possible for some of the exams. About this we will inform you in detail as soon as possible.
- 3. We intend to offer the exams of the 5th semester of the bachelor degree programs that were scheduled to take place in Examination Period 2 at the end of March as face-to-face exams at the end of May (25 to 29 May). This applies in





particular to the exams in "Leistungselektronik" (LE), "Weitverkehrsnetze" (WV), "Numerische Mathematik" (NM), "Elektromagnetische Verträglichkeit" (EV), "Maschinennahe Vernetzung" (MV) and "Produktdesign und Ergonomie" (PD), whose delay could otherwise slow down the graduation of students who are now in the sixth semester. Currently, however, it is not even possible to guarantee that these exams can be carried out at the end of May, because unfortunately there is still a serious bureaucratic/legal obstacle to be mastered in addition to the necessary coordination with the examiners involved. If possible, these exams will only be open to students who were registered for the Examination Period 2 in March.

We would also like to point out the following:

- 4. The number of attempts to pass the exams is suspended until further notice. Hence, nobody has to feel compelled to take a face-to-face exam.
- 5. Colloquia for bachelor or master theses and compositions (Ausarbeitungen) can usually be carried out online without any problem.
- 6. For students who only have very few exams (1-2) left to complete their studies, we intend to find exceptional solutions. Please contact the responsible Chair of the Examination Board (see also the announcement of 9 April 2020 at the departmental homepage <a href="https://www.th-owl.de/en/eecs">www.th-owl.de/en/eecs</a>).

We hope that this letter will help to clarify the situation.

Yours sincerely,

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(Deanery of the Department of Electrical Engineering and Computer Science)